

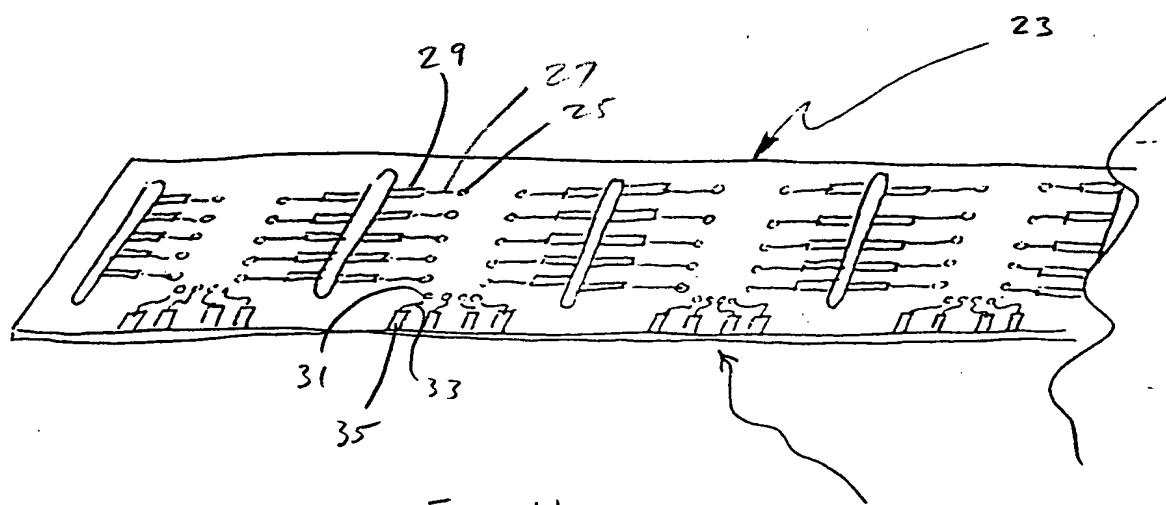
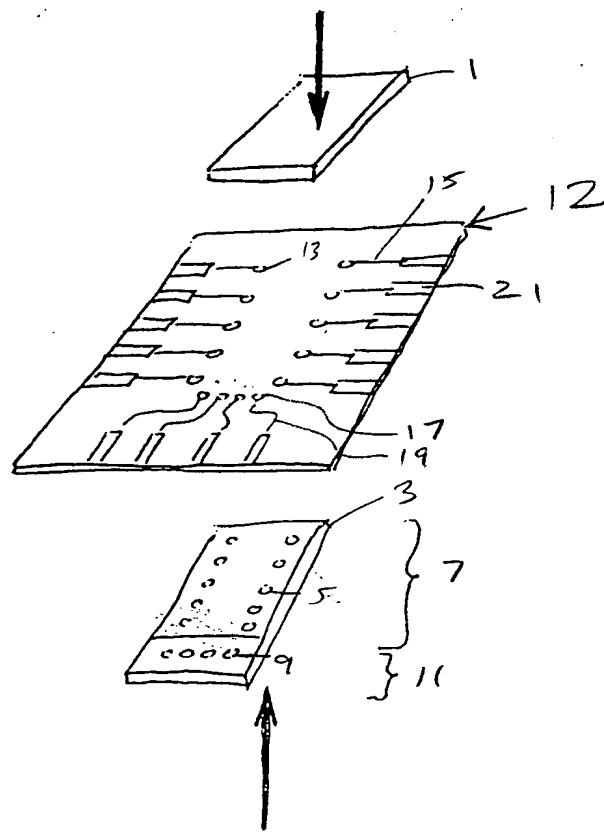
Fig. 1a
FIGURE 1A - FIG. 6660

Fig. 1b

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Fig. 2b

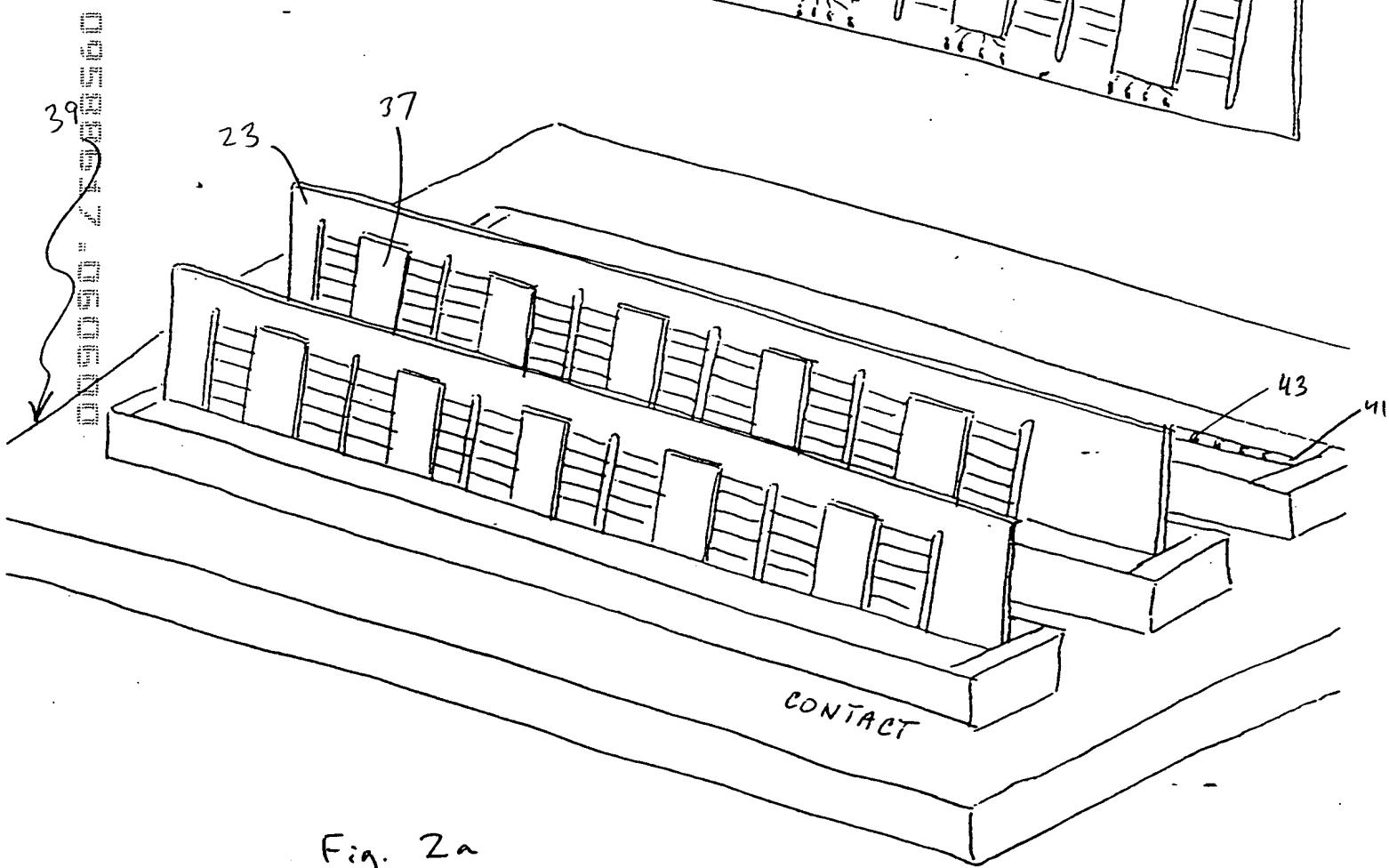
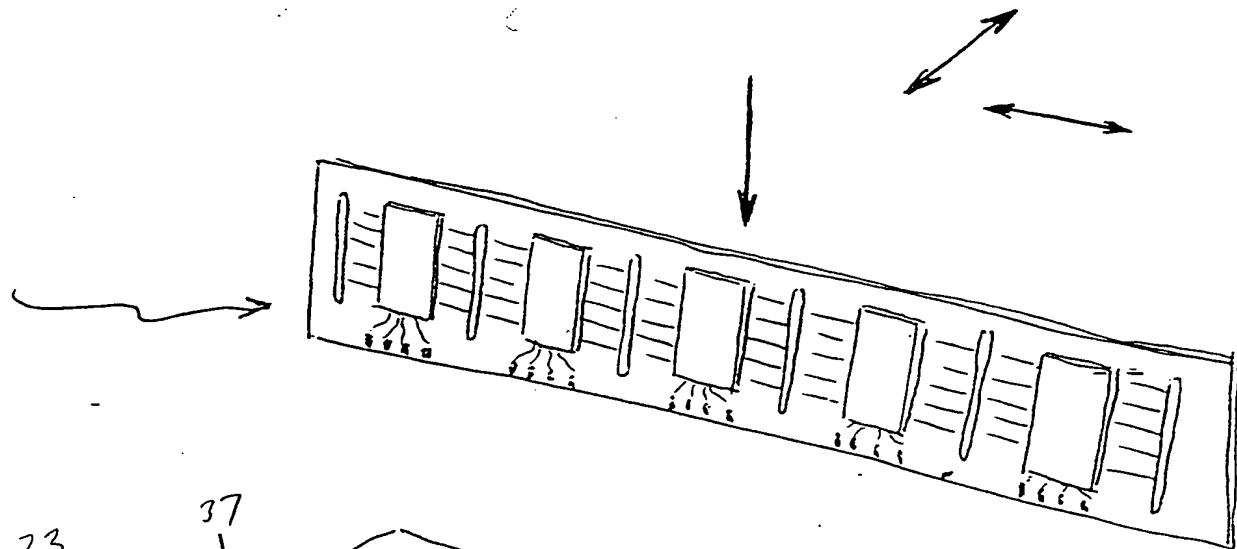


Fig. 2a

35611

0955647 - 060504

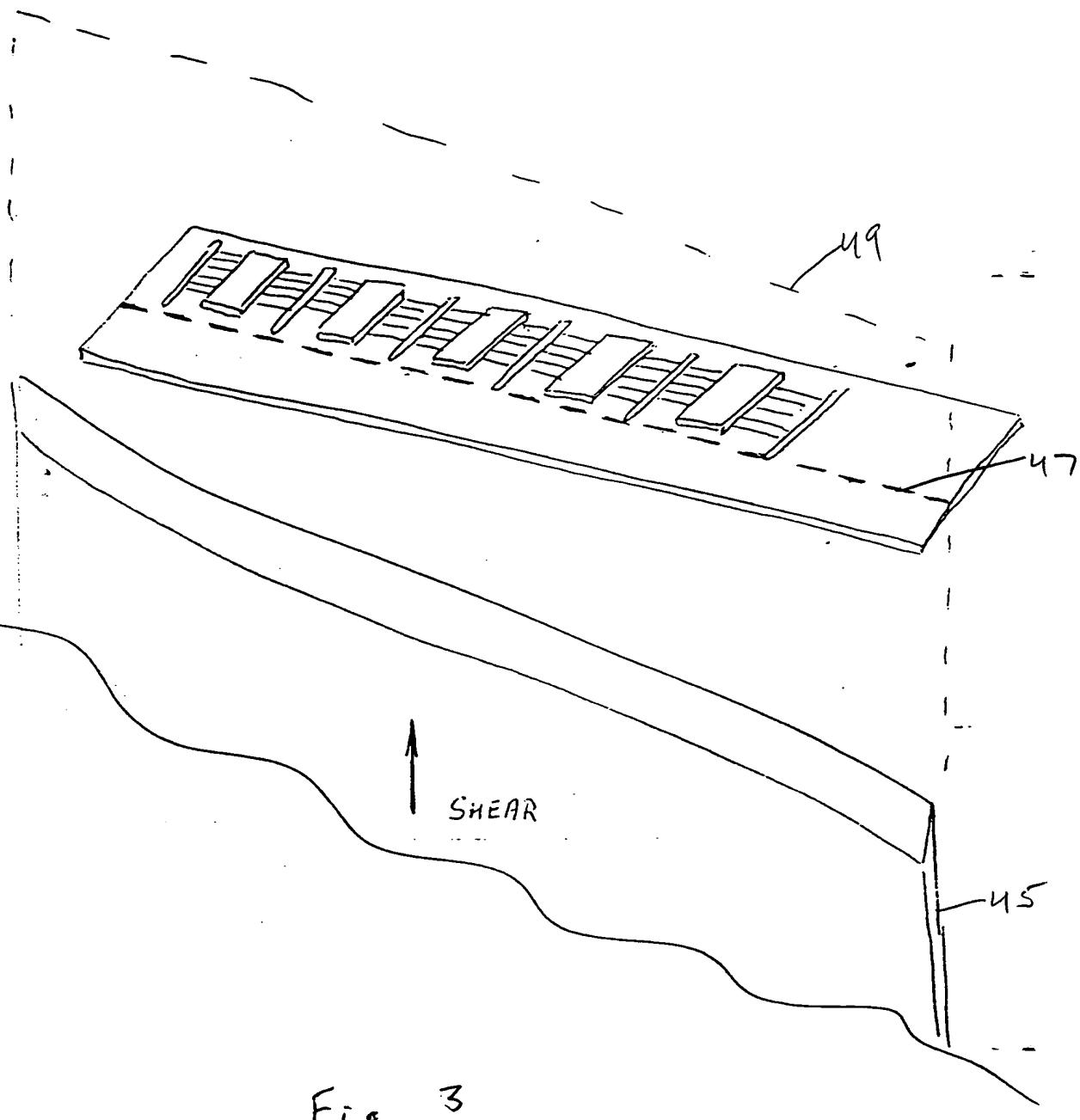


Fig. 3

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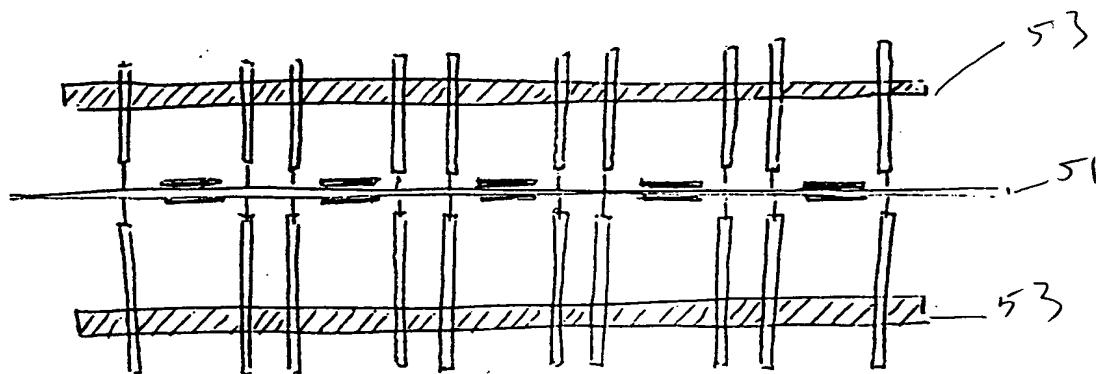


FIG 4.

0283617 060600

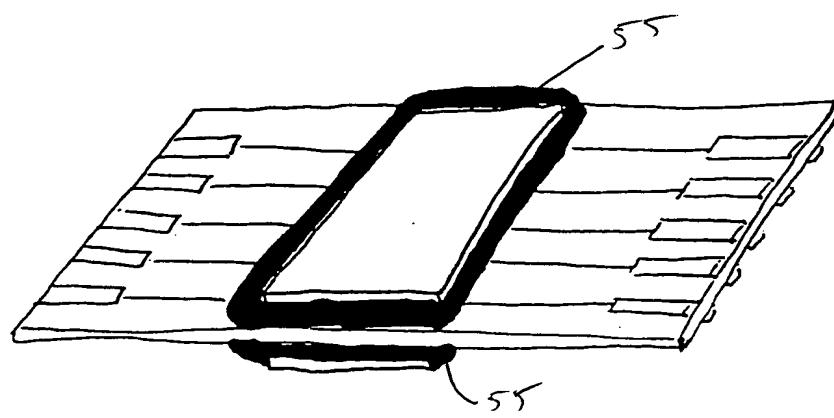


Fig. 5

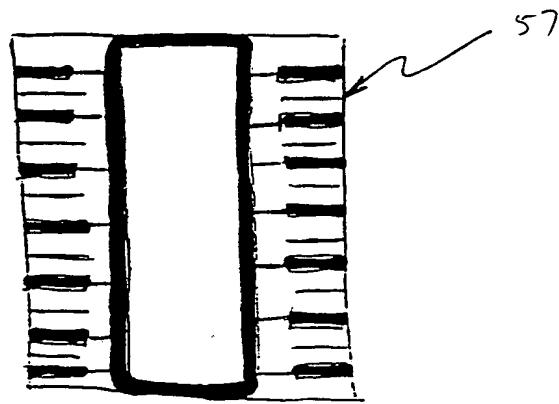


FIG. 6.

OPENING DRAWING

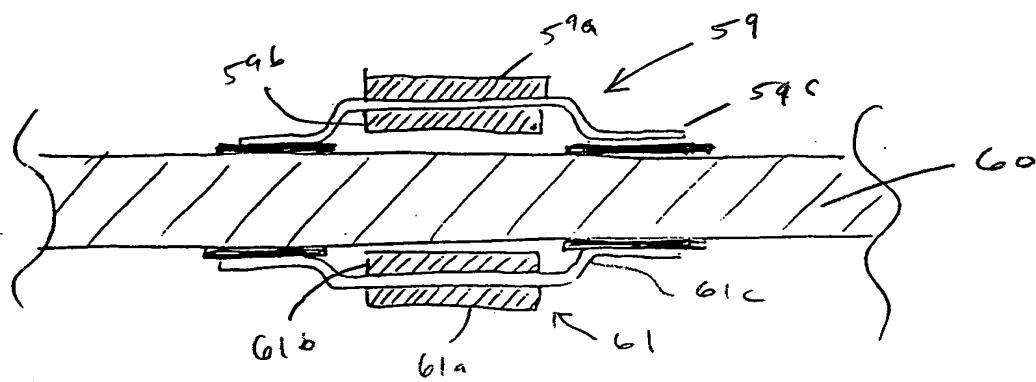


FIG. 7

62611

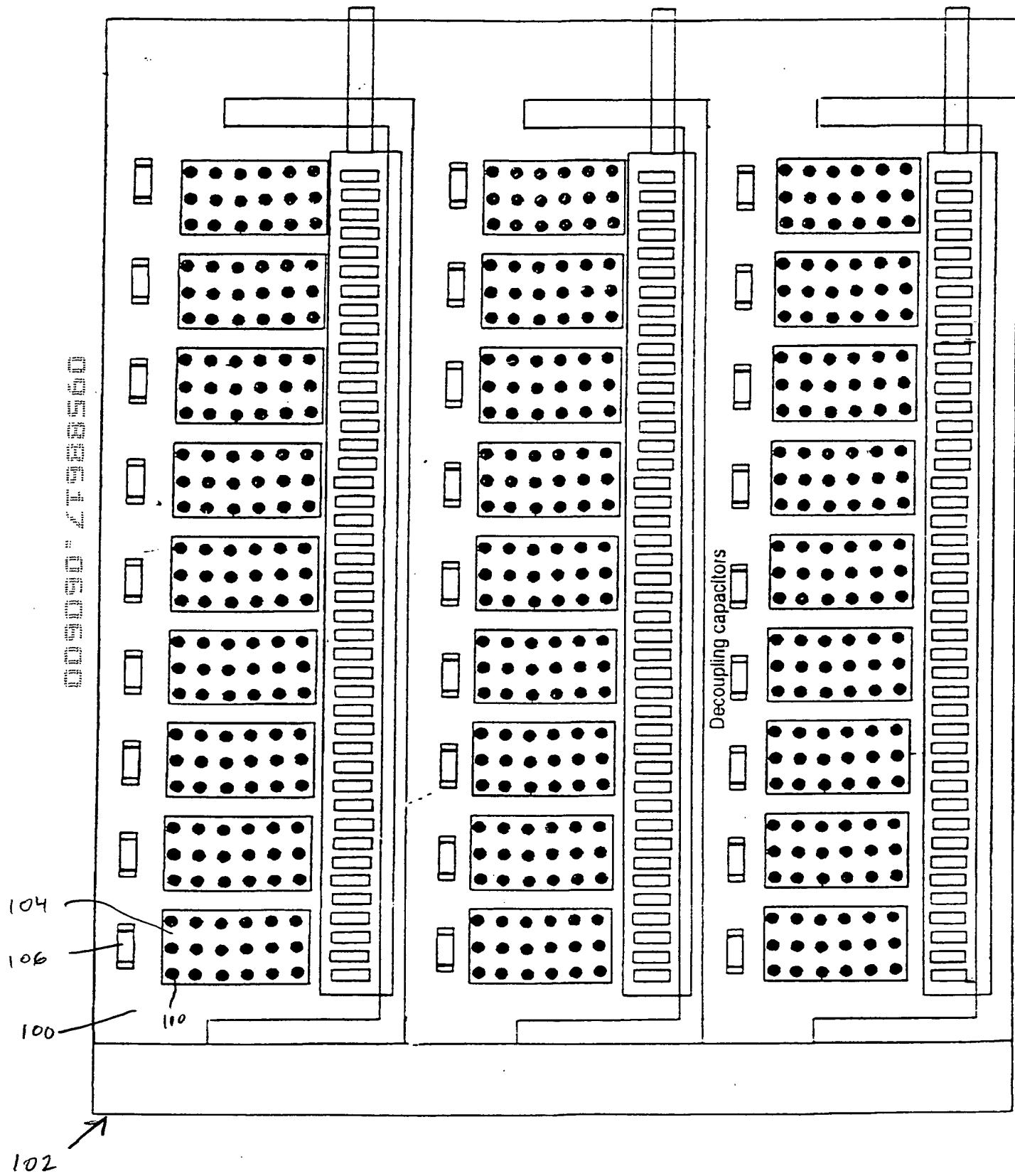


Fig. 8

00000000000000000000000000000000

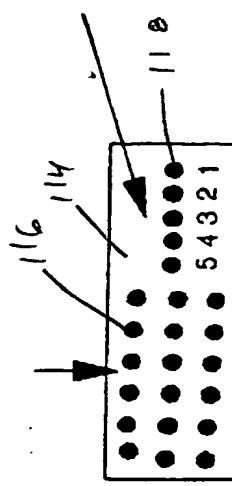


Fig. 9a

MEMORY (OR LOGIC) CHIP

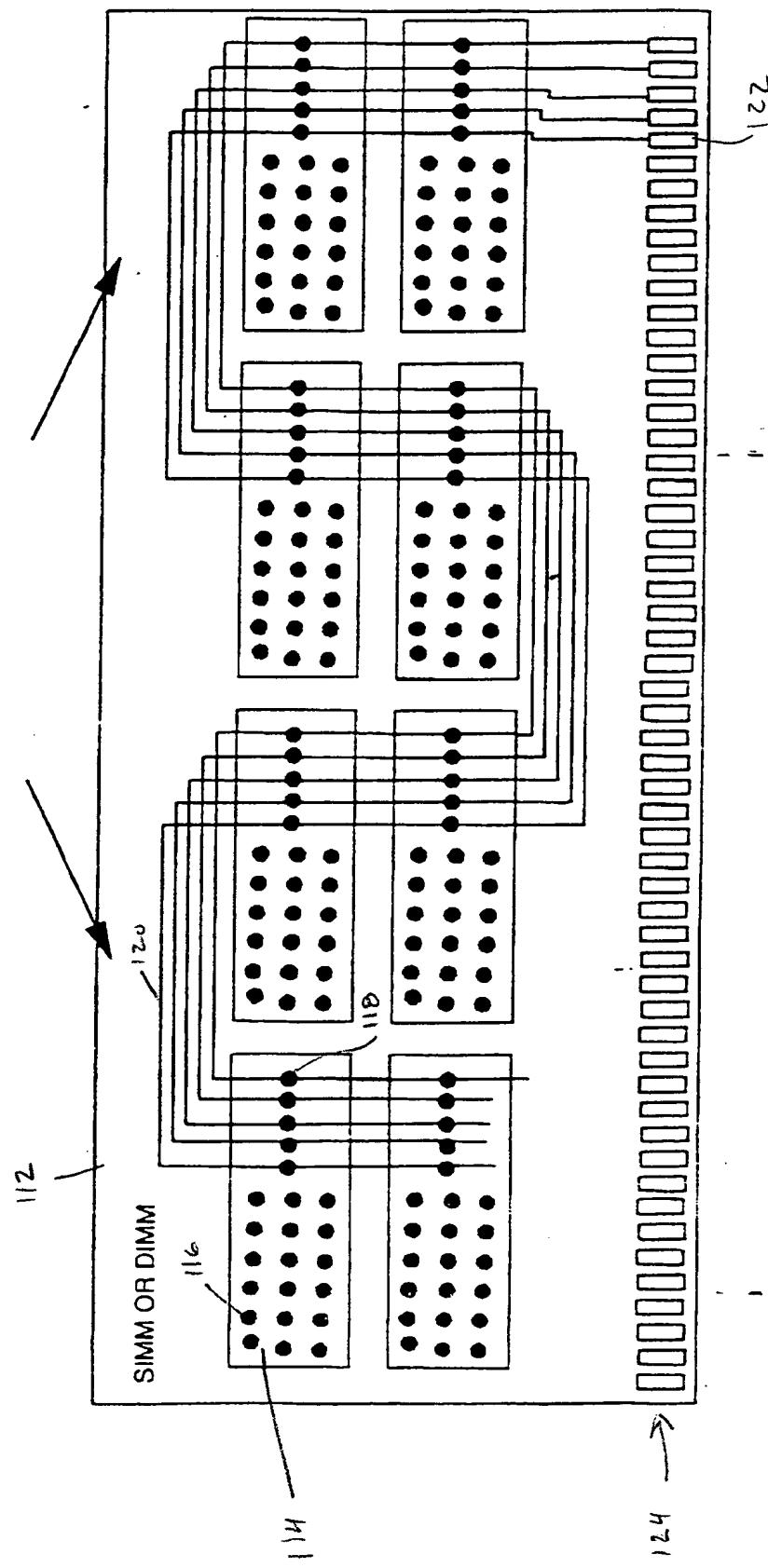


Fig. 9b

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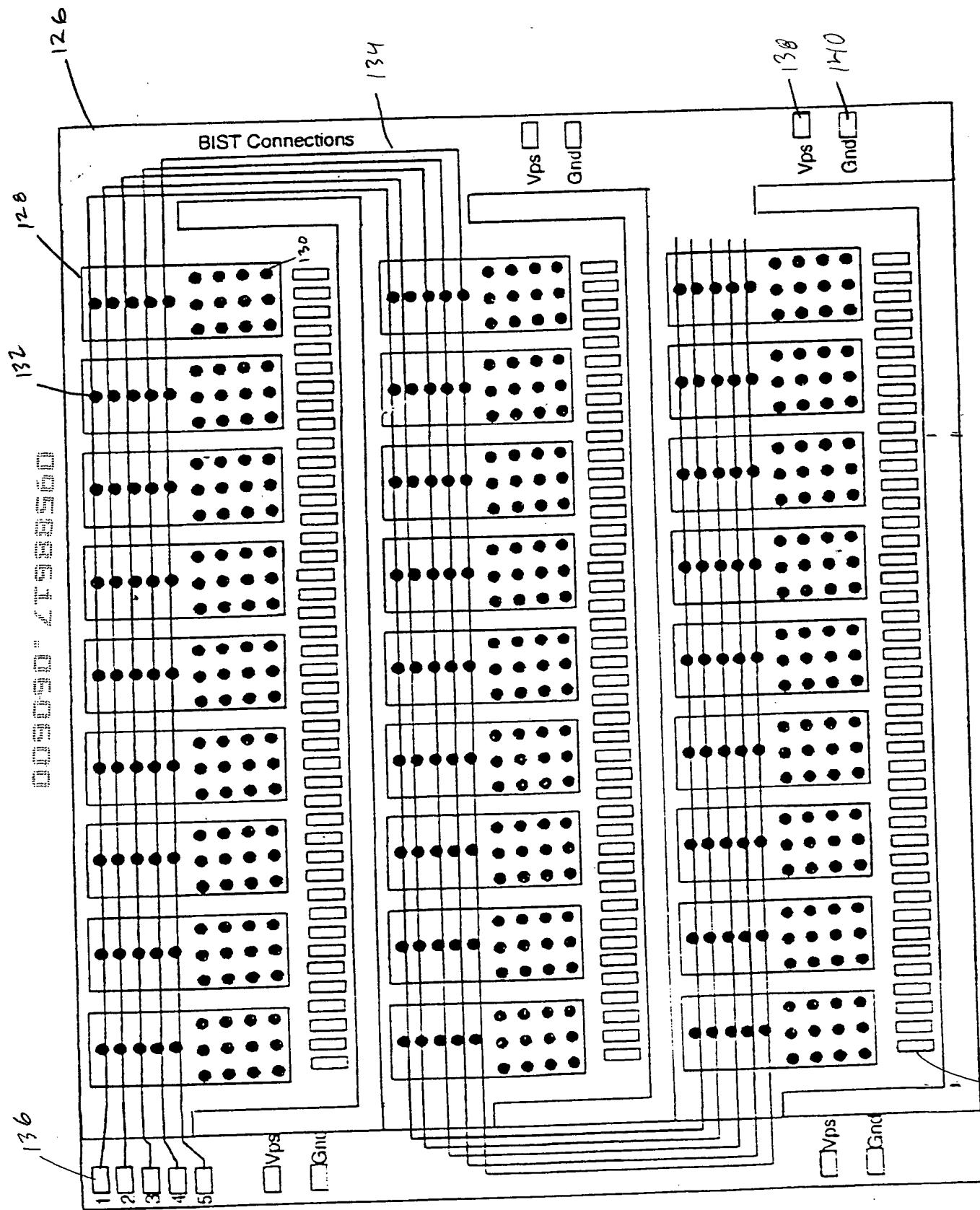
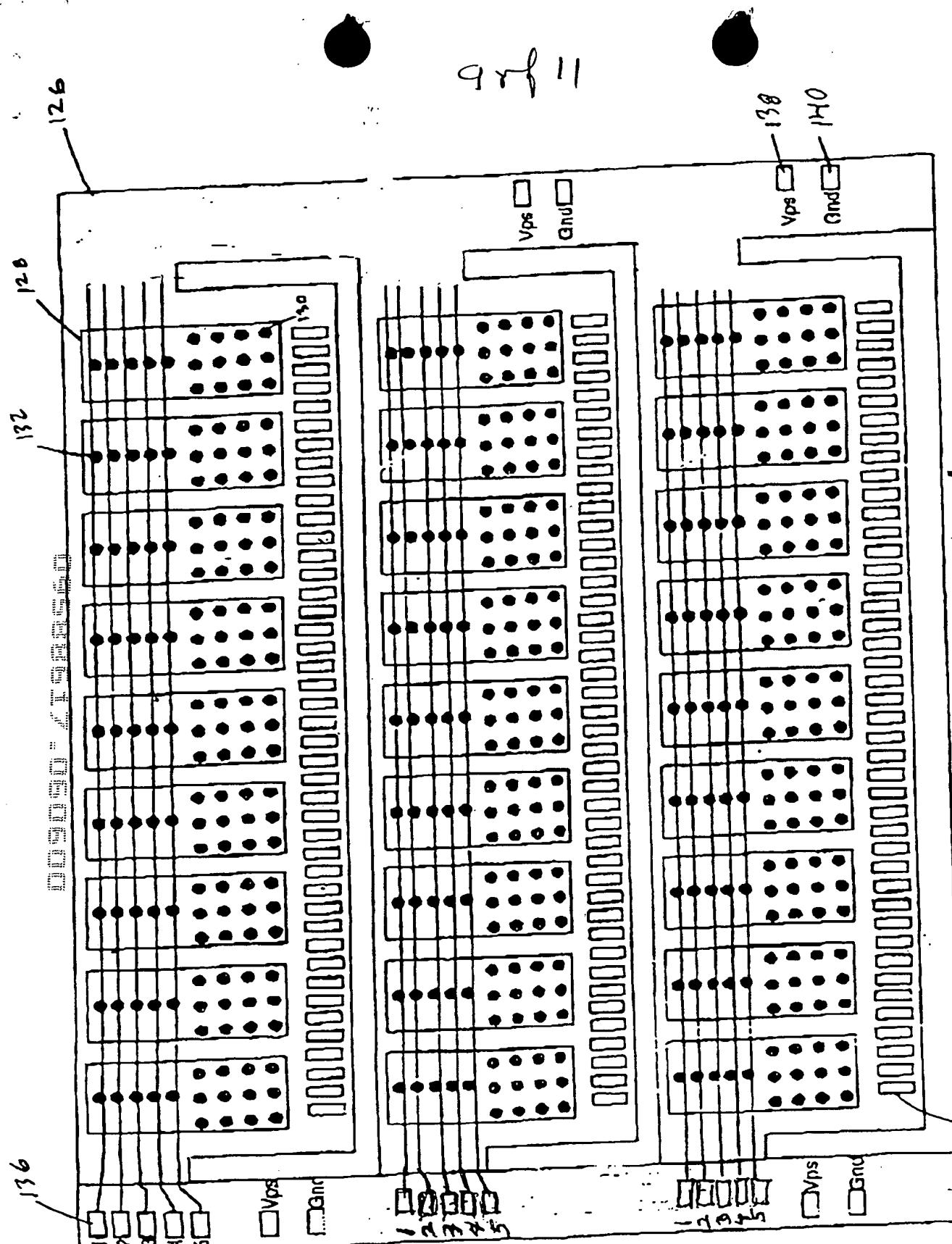


Fig. 10



Group 1

Group 2

Group 3

Fig 11

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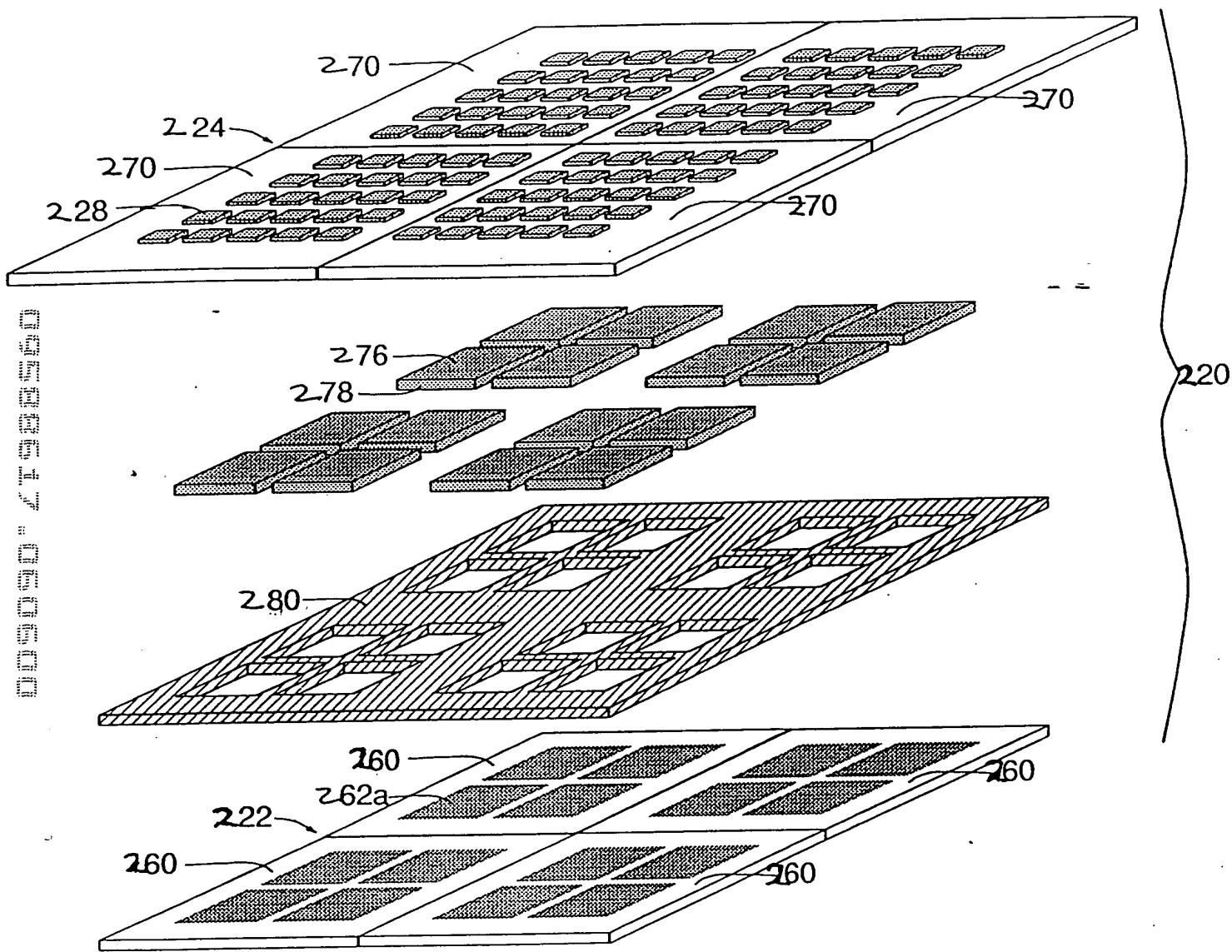


FIG. 1-2-

11/04/11

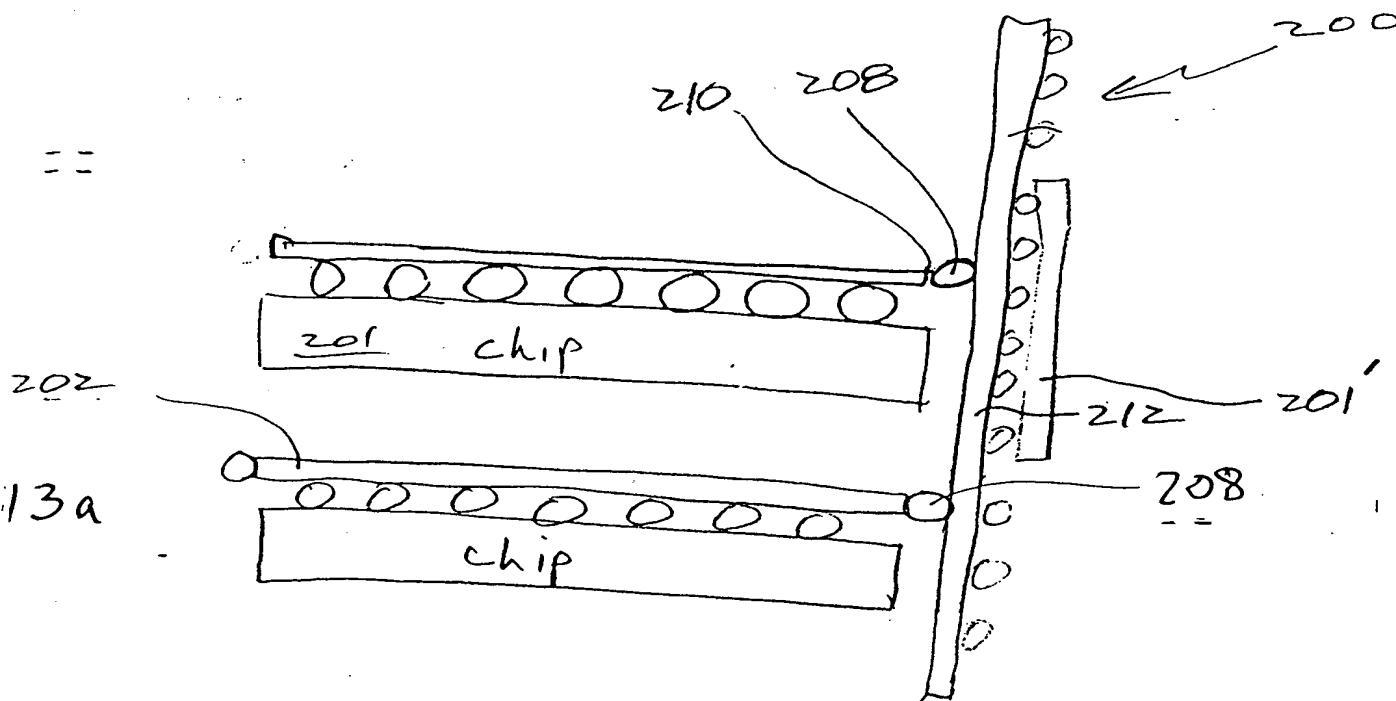


FIG. 13a

095338617 - 066500

FIG. 13b

